

ABSTRACT OF THE DISCLOSURE

An emissive display having pixels delimited by a plurality of scan lines and a plurality of signal lines intersecting with each other. Each pixel includes a memory circuit having at least a first inverter circuit including an electroluminescent device and including a display control circuit connecting in series a main circuit of at least one first transistor. The memory circuit stores display information of the pixel according to a conduction state or a non-conduction state of the main circuit of the first inverter, and controls an on state and an off state of the electroluminescent device.